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# FPGA Design Engineer, 150000 грн.

📍 Київ, 🕒 25 вересня 2024

Компанія: [Mobulusnet LTD](https://jobs.ua/company/id/1619413) (<https://jobs.ua/company/id/1619413>) ([Всі вакансії](#))

Рубрики: [IT, WEB фахівці](#), [Телекомунікація та зв'язок](#)

## Побажання до співробітника

Освіта: не має значення  
Досвід роботи: обов'язковий  
Графік роботи: віддалена робота

## Опис вакансії

### FPGA Design Engineer, 3000-3500\$

[mobulusnet.com](https://mobulusnet.com)  
[yulia@mobulusnet.com](mailto:yulia@mobulusnet.com)

### Job Responsibilities:

- Primary responsibilities include implementing, debugging, and verifying designs in Lattice FPGA
- The main task will be to design different video converters, like SDI to MIPI CSI, coaXpress to MIPI etc.,

### Required Skills & Experience

- Bachelor's degree in Electronics, Electrical Engineering, or a related field. Candidates without a degree but with significant experience can also be considered.
- 5+ years of experience in digital logic design, debugging, simulation, and FPGA implementation.
- Proficiency in hardware description languages such as Verilog or System Verilog.
- Experience with Lattice (preferred) or Intel/Altera FPGAs.
- Experience with Lattice Diamond/Radiant tools or Intel Quartus.
- Proficiency in FPGA simulation tools such as Modelsim or Questasim.
- Understanding of FPGA timing analysis and the ability to solve timing-related issues.
- Solid knowledge of various interfaces, including UART, SPI, I2C, etc.
- Hands-on experience with lab equipment such as logic analyzers and oscilloscopes.

### Nice to have:

- Experience with SDI, MIPI, coaXpress interfaces.
- Experience with Nvidia Jetson Orin board or similar.
- Experience in C language.
- Experience in writing device drivers for Linux.

Work online

Compliance with the deadlines

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yulia@mobulusnet.com

### **Контактна інформація**

Телефон: +38 (063) 997-01-69

Контактна  
особа: ЮліяКоробенко

Сайт: <https://mobulusnet.com>